



**Micro Commercial Components** 

Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 MT110CB08T1 MT110CB12T1 MT110CB16T1 MT110CB18T1

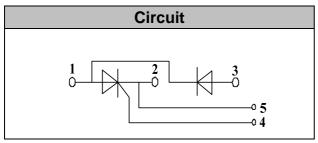
# **Features**• Lead Free Finish/RoHS Comp

- Lead Free Finish/RoHS Compliant (NOTE 1)("P" Suffix designates RoHS Compliant. See ordering information)
- International standard package
- Heat transfer through aluminum oxide DBC ceramic isolated metal baseplate
- Glass passivated chip
- Simple Mounting

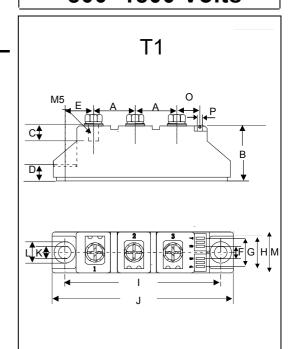
# **Applications**

- Power Converters
- · Lighting Control
- DC Motor Control and Drives
- · Heat and temperature control





# 110 Amp THYRISTOR/DIODE MODULE 800~1800 Volts



DIMENSIONS					
	INCHES		ММ		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.776	.799	19.70	20.30	
В	1.169	1.193	29.70	30.30	
С	.343	.366	8.70	9.30	
D	.323	.346	8.20	8.80	
Е	.602	.622	15.30	15.80	
F	.224	.248	5.70	6.30	
G	.539	.563	13.70	14.30	
Н	.657	.681	16.70	17.30	
1	3.138	3.161	79.70	80.30	
J	3.650	3.673	92.70	93.30	
K	.25	56	6.	50	Ø
L	.421	.445	10.70	11.30	
М	.815	.839	20.70	21.30	
0	.579	.602	14.70	15.30	
Р	0.11	X0.032	2.8)	X0.8	



**Module Type** 

TYPE	<b>V</b> RRM	Vrsm
MT110CB08T1	800V	900V
MT110CB12T1	1200V	1300V
MT110CB16T1	1600V	1700V
MT110CB18T1	1800V	1900V

#### **♦**Diode

**Maximum Ratings** 

Symbol	Item	Conditions	Values	Units
ID	Output Current(D.C.)	Tc=85℃	110	Α
IFSM	Surge forward current	t=10mS Tvj =45℃	2250	Α
i <sup>2</sup> t	Circuit Fusing Consideration		25000	$A^2s$
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
Tvj	Operating Junction Temperature		-40 to +125	$^{\circ}$
Tstg	Storage Temperature		-40 to +125	$^{\circ}$
Mt	Mounting Torque	To terminals(M5)	3±15%	Nm
Ms		To heatsink(M6)	5±15%	Nm
Weight	Module (Approximately)		100	g

#### **Thermal Characteristics**

Symbol	Item	Conditions	Values	Units
Rth(j-c)	Thermal Impedance, max.	Junction to Case	0.14	°C/W
Rth(c-s)	Thermal Impedance, max.	Case to Heatsink	0.10	°C/W

#### **Electrical Characteristics**

Symbol	Item	Conditions	Values			Units
Syllibol	n.em	Conditions	Min.	Тур.	Max.	Office
VFM	Forward Voltage Drop, max.	T=25°C IF =300A			1.65	V
IRRM	Repetitive Peak Reverse Current, max.	Tvj =25℃ VRD=VRRM Tvj =125℃ VRD=VRRM		≤0.5 ≤6		mA mA



## **♦**Thyristor

**Maximum Ratings** 

Symbol	Item	Conditions	Values	Units
I <sub>TAV</sub>	Average On-State Current	Sine 180°;Tc=85°C	110	Α
I <sub>TSM</sub>	Surge On-State Current	$T_{VJ}$ =45°C t=10ms, sine $T_{VJ}$ =125°C t=10ms, sine	2250 1900	Α
i <sup>2</sup> t	Circuit Fusing Consideration	$T_{VJ}$ =45°C t=10ms, sine $T_{VJ}$ =125°C t=10ms, sine	25000 18000	A2s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
Tvj	Operating Junction Temperature		-40 to +130	$^{\circ}$
Tstg	Storage Temperature		-40 to +125	${\mathbb C}$
Mt	Mounting Torque	To terminals(M5)	$3\pm15\%$	Nm
Ms		To heatsink(M6)	5±15%	Nm
di/dt	Critical Rate of Rise of On-State Current	$T_{VJ}$ = $T_{VJM}$ , 2/3 $V_{DRM}$ , $I_{G}$ =500mA Tr<0.5us,tp>6us	150	A/us
dv/dt	Critical Rate of Rise of Off-State Voltage, min.	T <sub>J</sub> =T <sub>VJM</sub> ,2/3V <sub>DRM</sub> linear voltage rise	1000	V/us
а	Maximum allowable acceleration		50	m/s <sup>2</sup>

### **Thermal Characteristics**

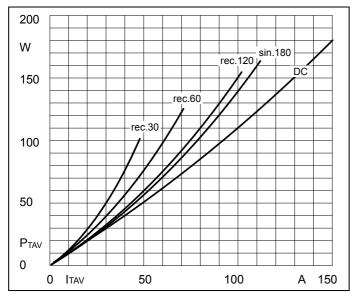
Symbol	Item	Conditions	Values	Units
Rth(j-c)	Thermal Impedance, max.	Junction to Case	0.28	°C/W
Rth(c-s)	Thermal Impedance, max.	Case to Heatsink	0.20	°C/W

#### **Electrical Characteristics**

Cumbal	Itam	Conditions	Value	S	Unito
Symbol	Item	Conditions			Units
V <sub>TM</sub>	Peak On-State Voltage, max.	T=25℃ I <sub>T</sub> =300A		1.65	V
I <sub>RRM</sub> /I <sub>DRM</sub>	Repetitive Peak Reverse Current, max. / Repetitive Peak Off-State Current, max.	$T_{VJ}=T_{VJM}$ , $V_R=V_{RRM}$ , $V_D=V_{DRM}$		20	mA
V <sub>TO</sub>	On state threshold voltage	For power-loss calculations only (T <sub>VJ</sub> =125°C)		0.9	V
r <sub>T</sub>	Value of on-state slope resistance. max	$T_{VJ} = T_{VJM}$		2	mΩ
$V_{GT}$	Gate Trigger Voltage, max.	$T_{VJ}$ =25°C , $V_D$ =6V		3	V
I <sub>GT</sub>	Gate Trigger Current, max.	$T_{VJ}$ =25°C , $V_D$ =6V		150	mA
$V_{\sf GD}$	Non-triggering gate voltage, max.	T <sub>VJ</sub> =125℃,V <sub>D</sub> =2/3V <sub>DRM</sub>		0.25	V
I <sub>GD</sub>	Non-triggering gate current, max.	$T_{VJ}$ =125°C, $V_D$ =2/3 $V_{DRM}$		6	mA
IL	Latching current, max.	$T_{VJ}$ =25°C , $R_G$ = 33 $\Omega$	300	600	mA
I <sub>H</sub>	Holding current, max.	T <sub>VJ</sub> =25℃ , V <sub>D</sub> =6V	150	250	mA
tgd	Gate controlled delay time	TVJ=25℃, IG=1A, diG/dt=1A/us	1		us
tq	Circuit commutated turn-off time	$T_{VJ} = T_{VJM}$	100		us



#### **Performance Curves**



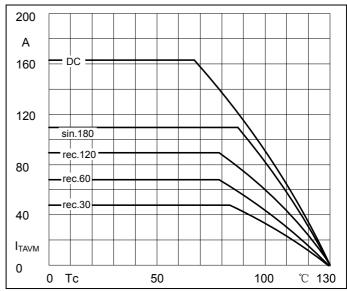


Fig1. Power dissipation

Fig2.Forward Current Derating Curve

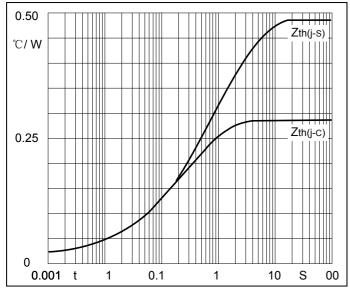


Fig3. Transient thermal impedance

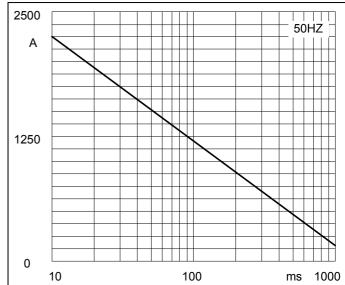


Fig4. Max Non-Repetitive Forward Surge Current



#### **Performance Curves**

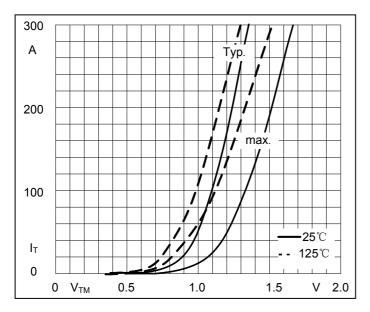


Fig5. Forward Characteristics

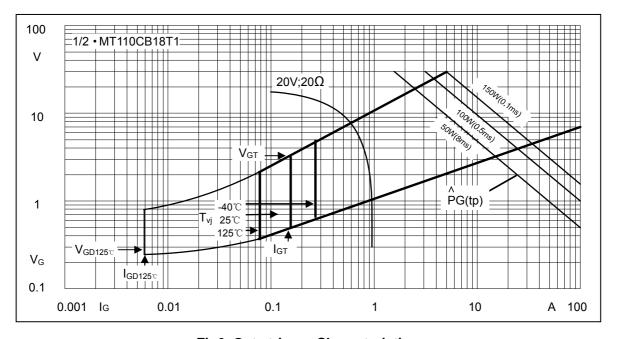


Fig6. Gate trigger Characteristics



#### **Ordering Information:**

Device	Packing
Part Number-BP	Bulk: 10PCS/BOX;100PCS/CTN

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